**SHRI MADHWA VADIRAJA INSTITUTE OF TECHNOLOGY & MANAGEMENT, BANTAKAL**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**TIME TABLE FOR ODD SEMESTER (2014-2015)**

**SEMESTER: III (A)**

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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | 9:15–10.15 | 10.15-11.15 | 11.15-11.30 | 11:30-12.30 | 12.30-1.30 | 1.30-2.30 | 2.30-3.30 | 3.30-4.30 |
| MON | LD  (EC012) | NA  (EC012) |  | EI  (EC012) | AEC  (EC012) |  | MAT3  (EC012) | FT  (EC012) |
| TUE | AEC LAB(A1)/LD LAB(A2) | | | | FT  (EC012) | LD  (EC012) | EI  (EC012) |
| WED | AEC LAB(A2)/LD LAB(A3) | | | | EI  (EC010) | AEC  (EC010) | MAT3  (EC010) |
| THU | AEC LAB(A3)/LD LAB(A1) | | | | NA  (EC010) | LD  (EC010) | FT  (EC010) |
| FRI | MAT3  (EC012) | LD  (EC012) |  | AEC  (EC012) | FT  (EC012) | EI  (EC012) | NA  (EC012) |
| SAT | NA  (EC012) | AEC  (EC012) | MAT3  (EC012) |  |  | |

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|  | LUNCH BREAK |

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| 10MAT31 | MAT3 | ENGINEERING MATHEMATICS-III | MRS. AISHWARYA |
| 10ES32 | AEC | ANALOG ELECTRONICS CIRCUITS | MR. AVINASH N J |
| 10ES33 | LD | LOGIC DESIGN | MS. VRUNDA ADKAR D |
| 10ES34 | NA | NETWORK ANALYSIS | MR. KRISHNA KUMAR P |
| 10IT35 | EI | ELECTRONIC INSTRUMENTATION | MR. SANDESH KUMAR |
| 10ES36 | FT | FIELD THEORY | MRS. RAJASHREE NAMBIAR |
| 10ESB37 | AEC LAB | ANALOG ELECTRONICS LAB | MR. RAJESH NAYAK, MR. ARUNA M S  MR. AVINASH N J , MR. RAJESH NAYAK  MR. AVINASH N J , MR. RAJESH NAYAK |
| 10ESB38 | LD LAB | LOGIC DESIGN LAB | MS. VRUNDA ADKAR D, MS.PRAJGNAMALA  MS. VRUNDA ADKAR D, MR. ARUN UPADHYAYA  MS. VRUNDA ADKAR D, MRS. RASHMI K R |

FACULTY ADVISORS: MS. VRUNDA ADKAR AND MR. SANDESH KUMAR

**HOD PRINCIPAL**

**SHRI MADHWA VADIRAJA INSTITUTE OF TECHNOLOGY & MANAGEMENT, BANTAKAL**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**TIME TABLE FOR ODD SEMESTER (2014-2015)**

**SEMESTER: III (B)**

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| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | 9:15–10.15 | | 10.15-11.15 | 11.15-11.30 | | | 11:30-12.30 | 12.30-1.30 | 1.30-2.30 | 2.30-3.30 | 3.30-4.30 |
| MON | AEC LAB(B2)/LD LAB(B3) | | | | | | | FT  (EC010) |  | EI  (EC010) | AEC  (EC010) |
| TUE | NA  (EC012) | AEC  (EC012) | | |  | MAT3  (EC012) | |  | AEC LAB(B1)/LD LAB(B2) | | |
| WED | FT  (EC012) | LD  (EC012) | | | NA  (EC012) | | LD  (EC012) |  | MAT3  (EC012) | EI  (EC012) |
| THU | MAT3  (EC012) | FT  (EC012) | | | EI  (EC012) | | LD  (EC012) | NA  (EC012) | LD  (EC012) |
| FRI | AEC LAB(B3)/LD LAB(B1) | | | | | | |  | MAT3  (EC011) | AEC  (EC011) | NA  (EC011) |
| SAT | AEC  (EC201) | | FT  (EC201) |  | | | EI  (EC201) |  |  |  |  |

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|  | LUNCH BREAK |

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| 10MAT31 | MAT3 | ENGINEERING MATHEMATICS-III | MS. NISHA REENA NAZARETH |
| 10ES32 | AEC | ANALOG ELECTRONICS CIRCUITS | MR. VINAYA KUMAR S R |
| 10ES33 | LD | LOGIC DESIGN | MR. SACHIN S BHAT |
| 10ES34 | NA | NETWORK ANALYSIS | MR. NAGARAJA RAO |
| 10IT35 | EI | ELECTRONIC INSTRUMENTATION | MRS. RASHMI K R |
| 10ES36 | FT | FIELD THEORY | MR. ARUNA M S |
| 10ESB37 | AEC LAB | ANALOG ELECTRONICS LAB | MR. VINAYA KUMAR S R, MR. SACHIN PRABHU K  MR. VINAYA KUMAR S R, MRS. RAJASHREE NAMBIAR  MR. VINAYA KUMAR S R, MRS. RAJASHREE NAMBIAR |
| 10ESB38 | LD LAB | LOGIC DESIGN LAB | MR. SACHIN S BHAT, DR. BALACHANDRA ACHAR H V  MR. SACHIN S BHAT, MRS. RASHMI K R  MR. SACHIN S BHAT, MRS. RASHMI K R |

FACULTY ADVISORS MRS.RASHMI AND MR. VINAYA KUMAR

**HOD PRINCIPAL**

**SHRI MADHWA VADIRAJA INSTITUTE OF TECHNOLOGY & MANAGEMENT, BANTAKAL**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**TIME TABLE FOR ODD SEMESTER (2014-2015)**

**SEMESTER: V (A)**

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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | 9:15–10.15 | 10.15-11.15 | 11.15-11.30 | 11:30-12.30 | 12.30-1.30 | 1.30-2.30 | 2.30-3.30 | 3.30-4.30 |
| MON | M&E  (EC011) | DSP  (EC011) | BREAK | M&R  (EC011) | AC  (EC011) |  | ITC  (EC011) | CMOS  (EC011) |
| TUE | M&E  (EC011) | AC  (EC011) | DSP  (EC011) |  | AC+LIC LAB(A2)/DSP LAB(A3) | | |
| WED | AC  (EC011) | CMOS  (EC011) | M&R  (EC011) | AC+LIC LAB(A3)/DSP LAB(A1) | | |
| THU | M&E  (EC011) | AC  (EC011) | M&R  (EC011) | CMOS  (EC011) |  | ITC  (EC011) | DSP  (EC011) |
| FRI | DSP  (EC011) | M&E  (EC011) | ITC  (EC011) |  | AC+LIC LAB(A1)/DSP LAB(A2) | | |
| SAT | ITC  (EC011) | CMOS  (EC011) | M&R  (EC011) |  | | |

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|  | LUNCH BREAK |

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| 10AL41 | M&E | MANAGEMENT AND ENTREPRENEURSHIP | MS.SOWMYA BHAT |
| 10EC52 | DSP | DIGITAL SIGNAL PROCESSING | MR. AVINASH N J |
| 10EC53 | AC | ANALOG COMMUNICATION | MS. PRAJGNAMALA |
| 10EC54 | M&R | MICROWAVE AND RADAR | MR. ARUN UPADHYAYA |
| 10EC55 | ITC | INFORMATION THEORY AND CODING | MR. RAJESH NAYAK |
| 10EC56 | CMOS | FUNDAMENTALS OF CMOS VLSI | DR. BALACHANDRA ACHAR H V |
| 10ECL57 | DSP LAB | DIGITAL SIGNAL PROCESSING LAB | MR. ARUNA M S, MR. KRISHNA KUMAR P  MR.AVINASH N J, MR. SANDESH KUMAR  MR. AVINASH N J, DR. BALACHANDRA ACHAR H V |
| 10ECL58 | AC+LIC LAB | ANALOG COMMUNICATION +  LINEAR INTEGRATED CIRCUITS LAB | MS. PRAJGNAMALA, MRS. RASHMI K R  MS. PRAJGNAMALA, MRS. SHASHIKALA R  MS. PRAJGNAMALA, MR. SACHIN PRABHU K |

FACULTY ADVISORS: MS. SHAREEN NORONHA AND MR. SACHIN S BHAT

**HOD PRINCIPAL**

**SHRI MADHWA VADIRAJA INSTITUTE OF TECHNOLOGY & MANAGEMENT, BANTAKAL**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**TIME TABLE FOR ODD SEMESTER (2014-2015)**

**SEMESTER: V (B)**

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| --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | 9:15–10.15 | 10.15-11.15 | 11.15-11.30 | 11:30-12.30 | 12.30-1.30 | 1.30-2.30 | 2.30-3.30 | 3.30-4.30 |
| MON | DSP  (EC201) | M&R  (EC201) | BREAK | AC  (EC201) |  | AC+LIC LAB(B1)/ DSP LAB(B2) | | |
| TUE | AC  (EC201) | M&R  (EC201) | M&E  (EC201) | CMOS  (EC201) |  | ITC  (EC201) | CMOS  (EC201) |
| WED | ITC  (EC201) | CMOS  (EC201) | M&R  (EC201) | M&E  (EC201) |  | AC  (EC201) | DSP  (EC201) |
| THU | ITC  (EC201) | DSP  (EC201) | M&E  (EC201) |  | MINI PROJECT  (PROJECT /INNOVATION LAB) | | |
| FRI | M&E  (EC201) | AC  (EC201) | DSP  (EC201) | CMOS  (EC201) |  | M&R  (EC201) | ITC  (EC201) |
| SAT | AC+LIC LAB(B2)/DSP LAB(B1) | | | |  | | | |

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|  | LUNCH BREAK |

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| 10AL41 | M&E | MANAGEMENT AND ENTREPRENEURSHIP | MR. SACHIN PRABHU K |
| 10EC52 | DSP | DIGITAL SIGNAL PROCESSING | MR. NAGARAJA RAO |
| 10EC53 | AC | ANALOG COMMUNICATION | MRS. SHASHIKALA R |
| 10EC54 | M&R | MICROWAVE AND RADAR | MR. CHETAN R |
| 10EC55 | ITC | INFORMATION THEORY AND CODING | MR. ARUN UPADHYAYA |
| 10EC56 | CMOS | FUNDAMENTALS OF CMOS VLSI | MS. SOWMYA BHAT |
| 10ECL57 | DSP LAB | DIGITAL SIGNAL PROCESSING LAB | MR. NAGARAJA RAO, MS. SOWMYA BHAT  MR. NAGARAJA RAO, MR. KRISHNA KUMAR P |
| 10ECL58 | AC+LIC LAB | ANALOG COMMUNICATION +  LINEAR INTEGRATED CIRCUITS LAB | MRS. SHASHIKALA R, MR. ARUN UPADHYAYA  MRS. SHASHIKALA R, MR. SACHIN PRABHU K |

FACULTY ADVISORS: MR. CHETAN R AND MR. SACHIN PRABHU

**HOD PRINCIPAL**

**SHRI MADHWA VADIRAJA INSTITUTE OF TECHNOLOGY & MANAGEMENT, BANTAKAL**

**DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**

**TIME TABLE FOR ODD SEMESTER (2014-2015)**

**SEMESTER: VII**

|  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- |
|  | 9:15–10.15 | 10.15-11.15 | 11.15-11.30 | 11:30-12.30 | 12.30-1.30 | 1.30-2.30 | 2.30-3.30 | 3.30-4.30 |
| MON | CCN  (EC010) | ESD  (EC010) |  | PE  (EC010) |  | VLSI LAB(A3)/PE LAB(A1) | | |
| TUE | CCN  (EC010) | PE  (EC010) | DS (EC202) | OFC  (EC010) |  | CFV (EC010) | ESD  (EC010) |
| IP (EC010) | SP (EC202) |
| WED | CFV (EC010) | CCN  (EC010) | DS (EC202) |  | VLSI LAB(A2)/PE LAB(A3) | | |
| SP (EC202) | IP (EC010) |
| THU | OFC  (EC010) | CFV (EC010) | DS (EC202) |  | VLSI LAB(A1)/PE LAB(A2) | | |
| SP (EC202) | IP (EC010) |
| FRI | ESD  (EC010) | OFC  (EC010) | CFV (EC010) | DS (EC202) |  | CCN  (EC010) | PE  (EC010) |
| SP (EC202) | IP (EC010) |
| SAT | OFC  (EC010) | PE  (EC010) |  | ESD  (EC010) |  | | | |

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| 10EC71 | CCN | COMPUTER COMMUNICATION NETWORKS | MR. SACHIN PRABHU K |
| 10EC72 | OFC | OPTICAL FIBER COMMUNICATION | MRS. RASHMI K R |
| 10EC73 | PE | POWER ELECTRONICS | MS. SHAREEN NORONHA |
| 10EC74 | ESD | EMBEDDED SYSTEM DESIGN | MR. RAJESH NAYAK |
| 10EC754 | CFV | CAD FOR VLSI | MR. CHETAN R |
| 10EC756 | SP | SPEECH PROCESSING | MR. KRISHNA KUMAR P |
| 10EC761 | DS | DATA STRUCTURE USING C++ | MRS. DHRUVAJITA DEVI |
| 10EC763 | IP | IMAGE PROCESSING | MRS. SHASHIKALA R |
| 10ECL77 | VLSI LAB | VERY LARGE SCALE INTEGRATION LAB | MR. CHETAN R, MS. SOWMYA BHAT  MR. CHETAN R, MS. SOWMYA BHAT  MR. CHETAN R, MS. SOWMYA BHAT |
| 10ECL78 | PE LAB | POWER ELECTRONICS LAB | MS. SHAREEN NORONHA, MS. VRUNDA ADKAR D  MS. SHAREEN NORONHA, MRS. RAJASHREE NAMBIAR  MS. SHAREEN NORONHA, MRS. RAJASHREE NAMBIAR |

FACULTY ADVISORS: MRS. SHASHIKALA R AND MR. KRISHNA KUMAR P

**HOD PRINCIPAL**